

# IRF7343PbF

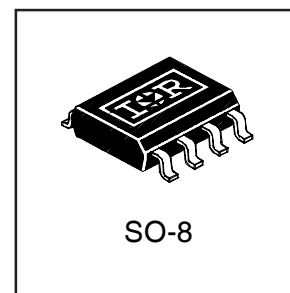
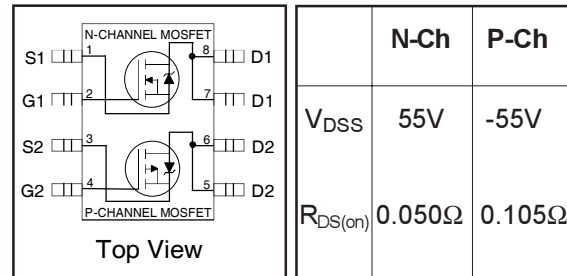
HEXFET® Power MOSFET

- Generation V Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Fully Avalanche Rated
- Lead-Free

## Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques.



## Absolute Maximum Ratings

Parameter	Description	Max.		Units
		N-Channel	P-Channel	
$V_{DS}$	Drain-Source Voltage	55	-55	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	4.7	-3.4	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	3.8	-2.7	
$I_{DM}$	Pulsed Drain Current ①	38	-27	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation ⑤	2.0		W
$P_D @ T_A = 70^\circ C$	Maximum Power Dissipation ⑤	1.3		W
$E_{AS}$	Single Pulse Avalanche Energy ③	72	114	mJ
$I_{AR}$	Avalanche Current	4.7	-3.4	A
$E_{AR}$	Repetitive Avalanche Energy	0.20		mJ
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$		V
dv/dt	Peak Diode Recovery dv/dt ②	5.0	-5.0	V/ns
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +150		°C

## Thermal Resistance

Parameter	Description	Typ.	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ⑤	---	62.5	°C/W

## Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

Parameter	Min.	Typ.	Max.	Units	Conditions	
						Parameter
V <sub>(BR)DSS</sub>	N-Ch	55	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
	P-Ch	-55	—	—		V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
ΔV <sub>(BR)DSS</sub> /ΔT <sub>J</sub>	N-Ch	—	0.059	—	V/°C	Reference to 25°C, I <sub>D</sub> = 1mA
	P-Ch	—	0.054	—		Reference to 25°C, I <sub>D</sub> = -1mA
R <sub>DS(ON)</sub>	N-Ch	—	0.043	0.050	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.7A ③
		—	0.056	0.065		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3.8A ④
	P-Ch	—	0.095	0.105		V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.4A ④
		—	0.150	0.170		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.7A ④
V <sub>GS(th)</sub>	N-Ch	1.0	—	—	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
	P-Ch	-1.0	—	—		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
g <sub>fs</sub>	N-Ch	7.9	—	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.5A ③
	P-Ch	3.3	—	—		V <sub>DS</sub> = -10V, I <sub>D</sub> = -3.1A ④
I <sub>DSS</sub>	N-Ch	—	—	2.0	μA	V <sub>DS</sub> = 55V, V <sub>GS</sub> = 0V
		—	—	-2.0		V <sub>DS</sub> = -55V, V <sub>GS</sub> = 0V
	P-Ch	—	—	25		V <sub>DS</sub> = 55V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55°C
		—	—	-25		V <sub>DS</sub> = -55V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55°C
I <sub>GSS</sub>	N-P	—	—	±100	nA	V <sub>GS</sub> = ±20V
Q <sub>g</sub>	N-Ch	—	24	36	nC	N-Channel I <sub>D</sub> = 4.5A, V <sub>DS</sub> = 44V, V <sub>GS</sub> = 10V ④
	P-Ch	—	26	38		
Q <sub>gs</sub>	N-Ch	—	2.3	3.4	nC	P-Channel I <sub>D</sub> = -3.1A, V <sub>DS</sub> = -44V, V <sub>GS</sub> = -10V ④
	P-Ch	—	3.0	4.5		
Q <sub>gd</sub>	N-Ch	—	7.0	10	ns	N-Channel V <sub>DD</sub> = 28V, I <sub>D</sub> = 1.0A, R <sub>G</sub> = 6.0Ω, R <sub>D</sub> = 16Ω ④
	P-Ch	—	8.4	13		
t <sub>d(on)</sub>	N-Ch	—	8.3	12	ns	P-Channel V <sub>DD</sub> = -28V, I <sub>D</sub> = -1.0A, R <sub>G</sub> = 6.0Ω, R <sub>D</sub> = 16Ω ④
	P-Ch	—	14	22		
t <sub>r</sub>	N-Ch	—	3.2	4.8	ns	N-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1.0MHz
	P-Ch	—	10	15		
t <sub>d(off)</sub>	N-Ch	—	32	48	ns	P-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V, f = 1.0MHz
	P-Ch	—	43	64		
t <sub>f</sub>	N-Ch	—	13	20	ns	P-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V, f = 1.0MHz
	P-Ch	—	22	32		
C <sub>iss</sub>	N-Ch	—	740	—	pF	N-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1.0MHz
P-Ch	—	690	—			
C <sub>oss</sub>	N-Ch	—	190	—	pF	P-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V, f = 1.0MHz
	P-Ch	—	210	—		
C <sub>rss</sub>	N-Ch	—	71	—	pF	P-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = -25V, f = 1.0MHz
	P-Ch	—	86	—		

## Source-Drain Ratings and Characteristics

Parameter	Min.	Typ.	Max.	Units	Conditions	
I <sub>S</sub>	N-Ch	—	—	2.0	A	
	P-Ch	—	—	-2.0		
I <sub>SM</sub>	N-Ch	—	—	38	A	
	P-Ch	—	—	-27		
V <sub>SD</sub>	N-Ch	—	0.70	1.2	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = 2.0A, V <sub>GS</sub> = 0V ③
	P-Ch	—	-0.80	-1.2		T <sub>J</sub> = 25°C, I <sub>S</sub> = -2.0A, V <sub>GS</sub> = 0V ③
t <sub>rr</sub>	N-Ch	—	60	90	ns	N-Channel T <sub>J</sub> = 25°C, I <sub>F</sub> = 2.0A, di/dt = 100A/μs ④
	P-Ch	—	54	80		
Q <sub>rr</sub>	N-Ch	—	120	170	nC	P-Channel T <sub>J</sub> = 25°C, I <sub>F</sub> = -2.0A, di/dt = 100A/μs ④
	P-Ch	—	85	130		

### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 22 )
- ② N-Channel I<sub>SD</sub> ≤ 4.7A, di/dt ≤ 220A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>J</sub> ≤ 150°C  
P-Channel I<sub>SD</sub> ≤ -3.4A, di/dt ≤ -150A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>J</sub> ≤ 150°C
- ③ N-Channel Starting T<sub>J</sub> = 25°C, L = 6.5mH R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 4.7A.  
P-Channel Starting T<sub>J</sub> = 25°C, L = 20mH R<sub>G</sub> = 25Ω, I<sub>AS</sub> = -3.4A.
- ④ Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ⑤ Surface mounted on FR-4 board, t ≤ 10sec.

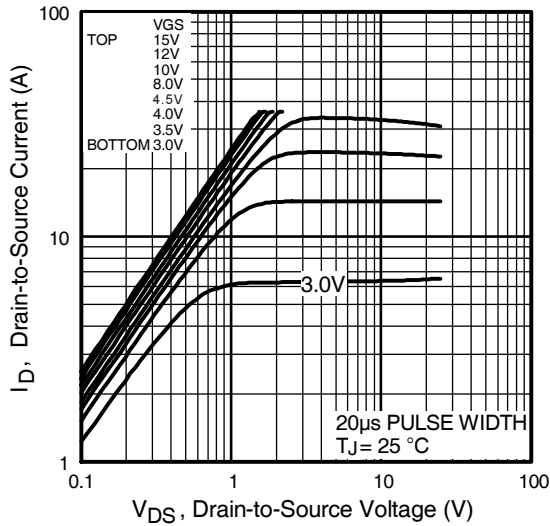


Fig 1. Typical Output Characteristics

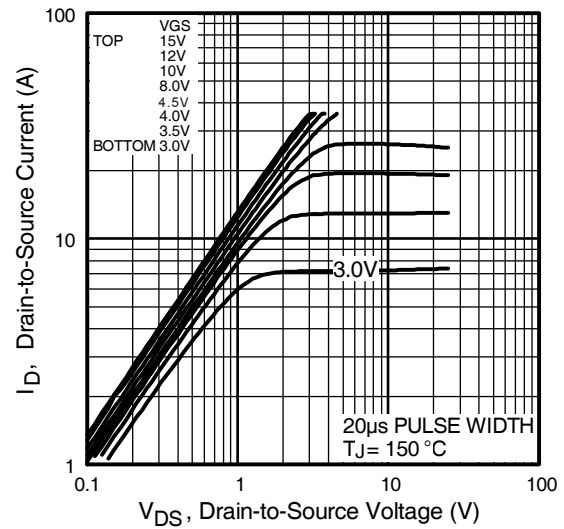


Fig 2. Typical Output Characteristics

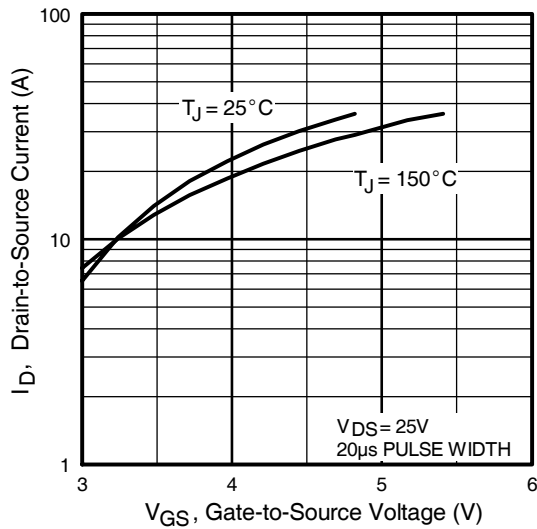


Fig 3. Typical Transfer Characteristics

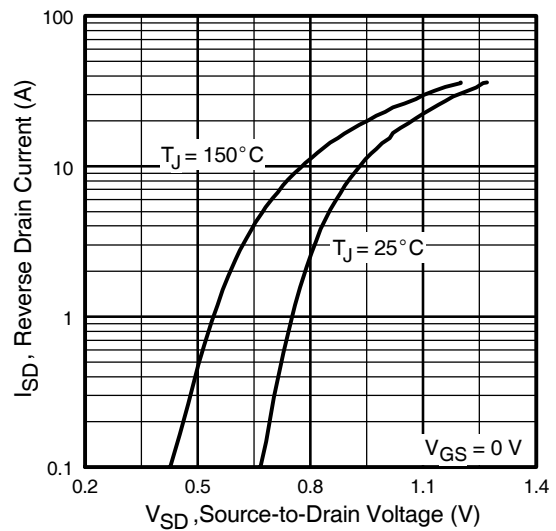
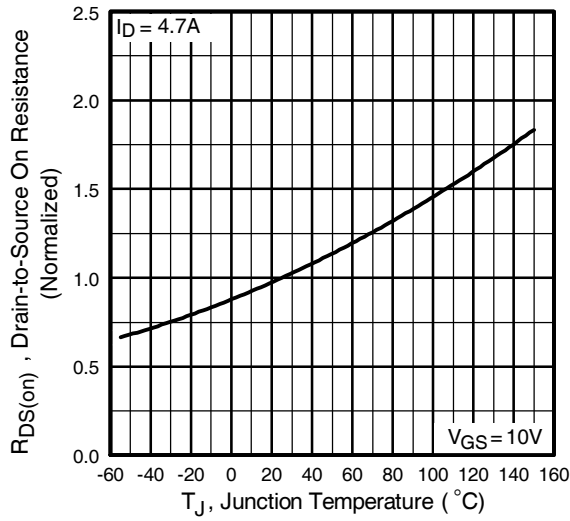
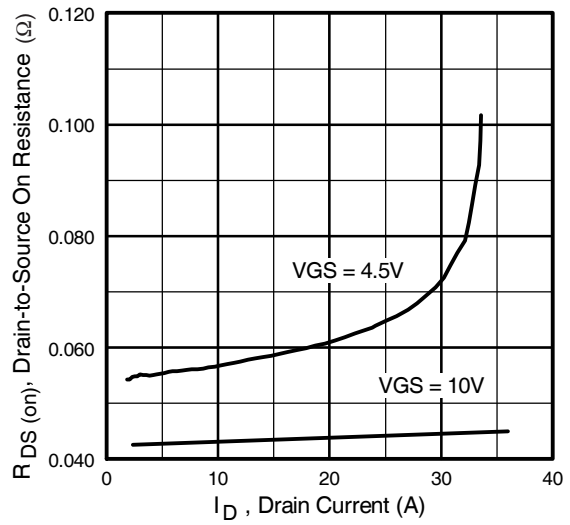


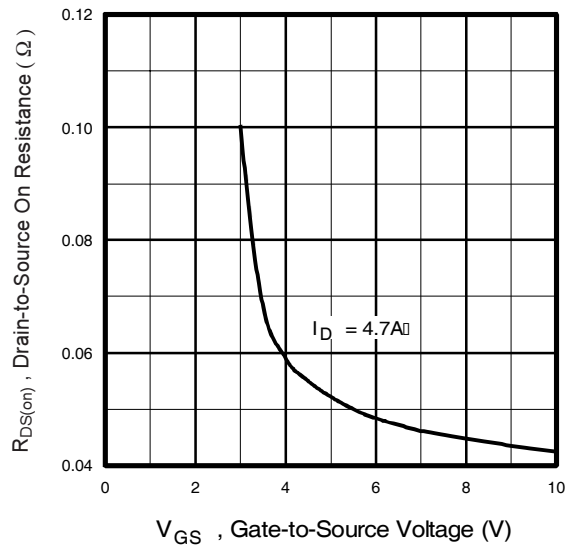
Fig 4. Typical Source-Drain Diode Forward Voltage



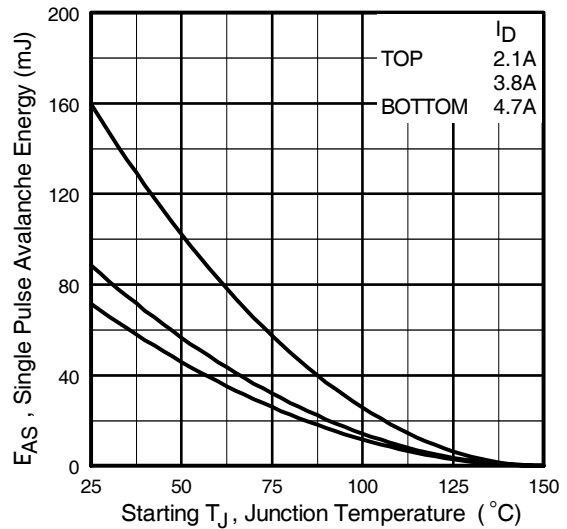
**Fig 5.** Normalized On-Resistance Vs. Temperature



**Fig 6.** Typical On-Resistance Vs. Drain Current



**Fig 7.** Typical On-Resistance Vs. Gate Voltage



**Fig 8.** Maximum Avalanche Energy Vs. Drain Current

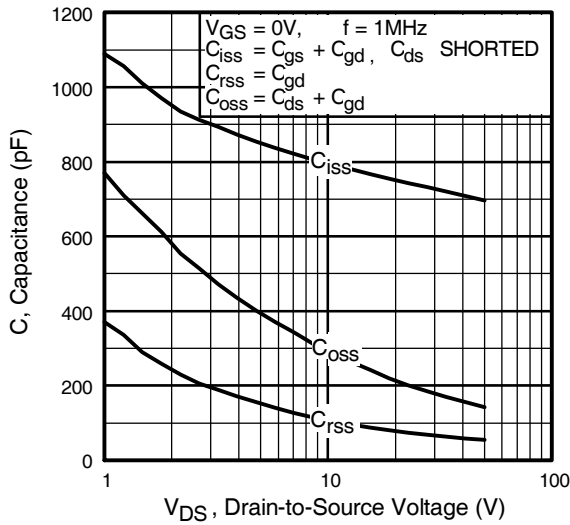


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

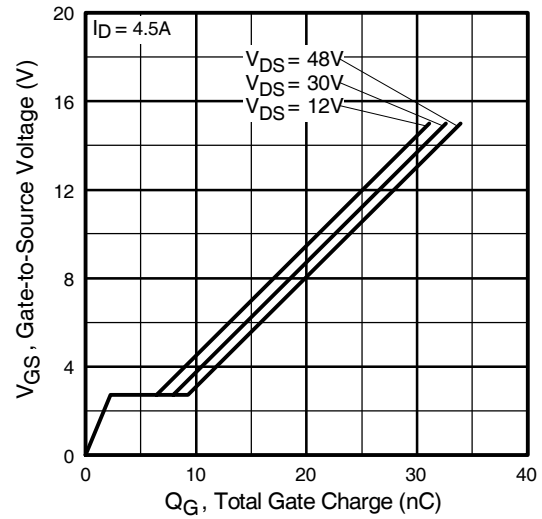


Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

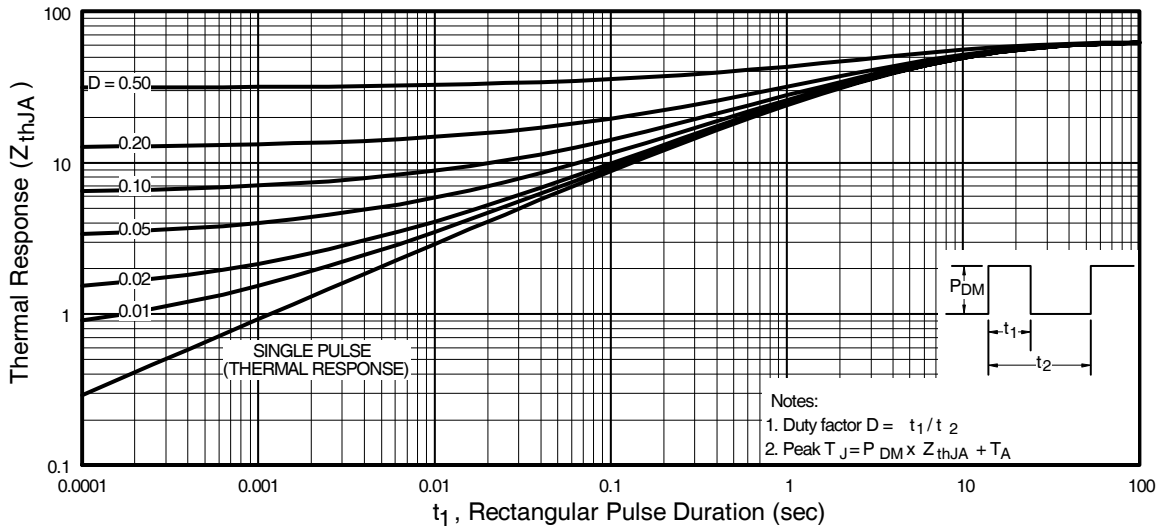
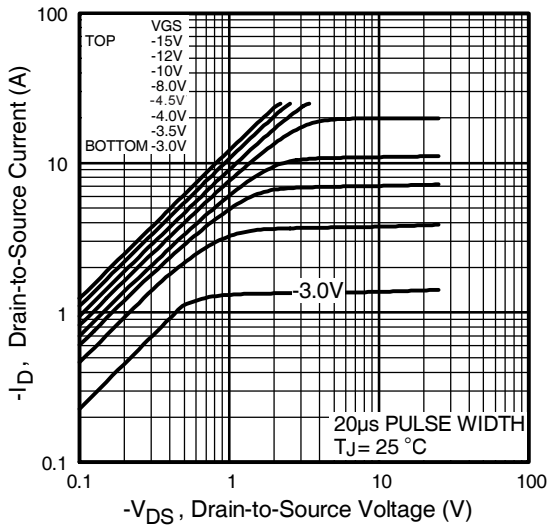
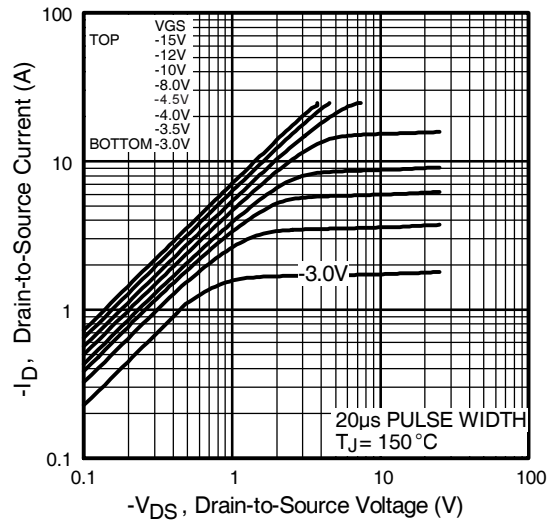


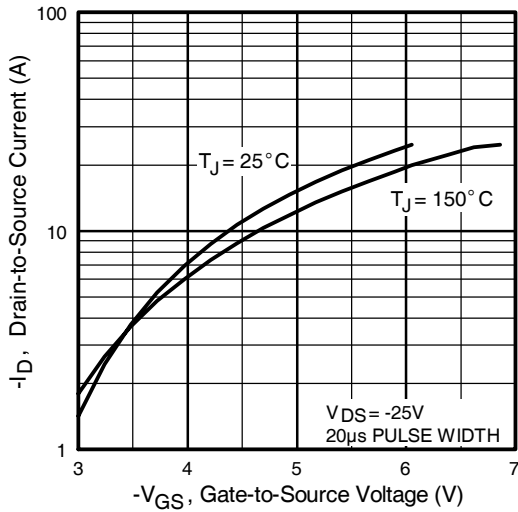
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



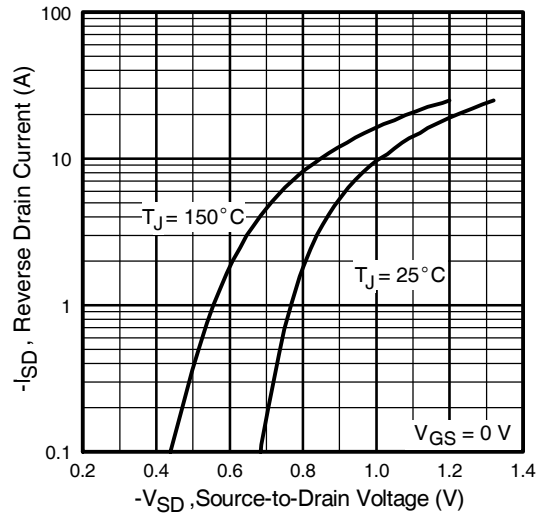
**Fig 12.** Typical Output Characteristics



**Fig 13.** Typical Output Characteristics



**Fig 14.** Typical Transfer Characteristics



**Fig 15.** Typical Source-Drain Diode Forward Voltage

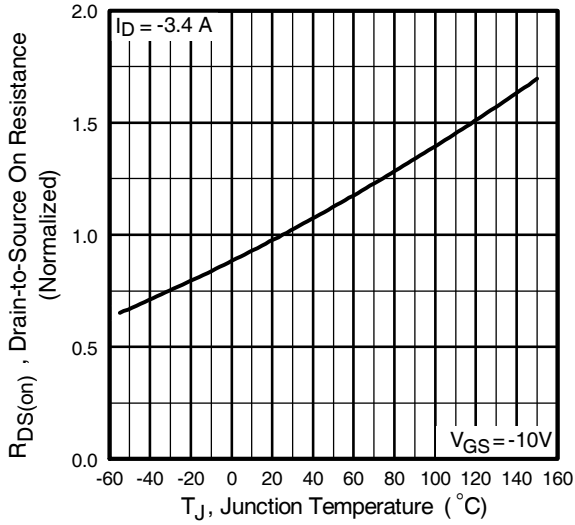


Fig 16. Normalized On-Resistance Vs. Temperature

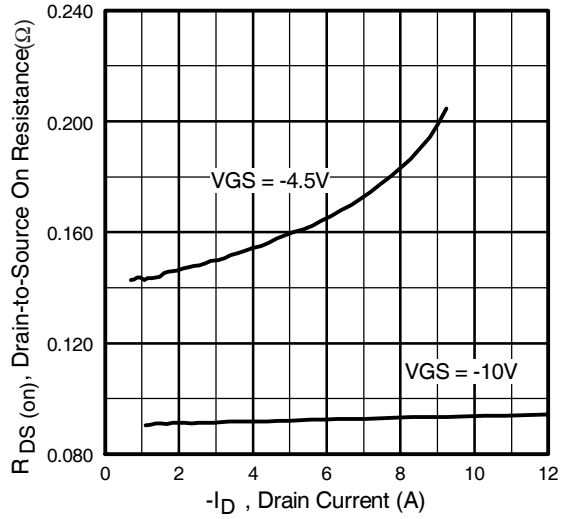


Fig 17. Typical On-Resistance Vs. Drain Current

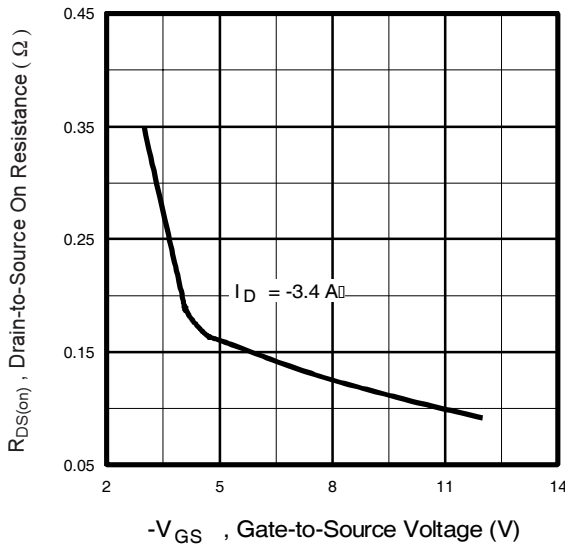


Fig 18. Typical On-Resistance Vs. Gate Voltage

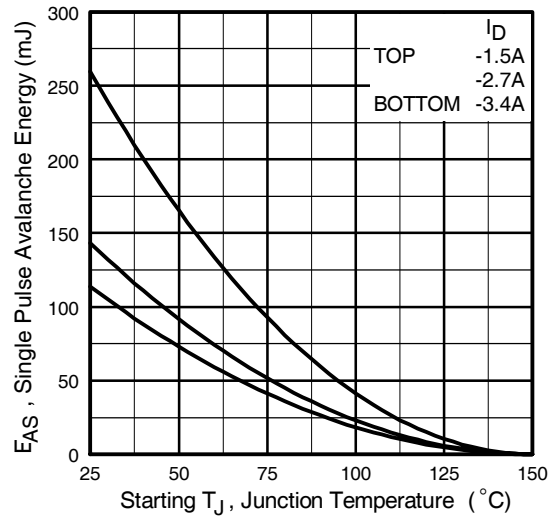
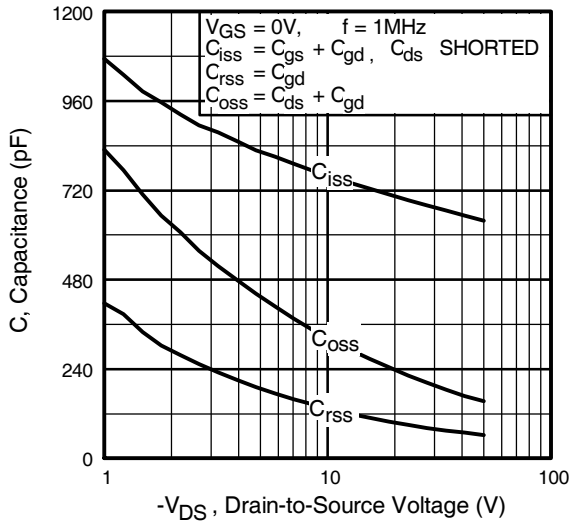
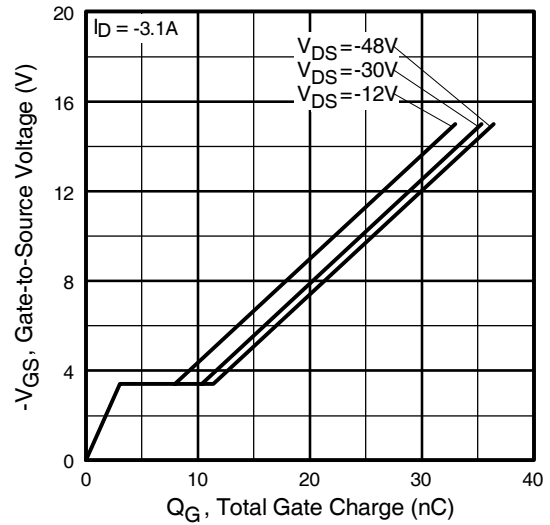


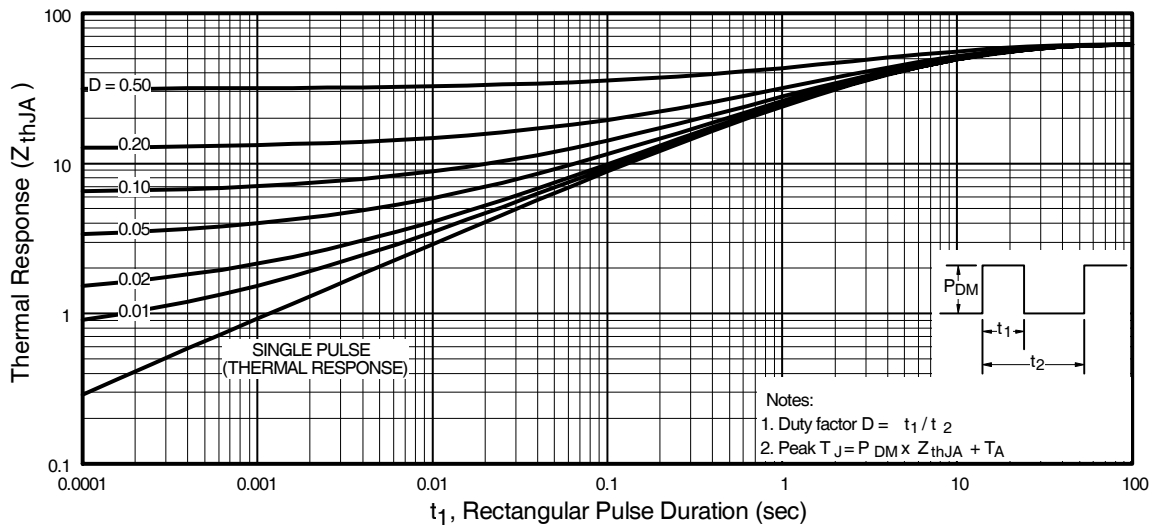
Fig 19. Maximum Avalanche Energy Vs. Drain Current



**Fig 20.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 21.** Typical Gate Charge Vs. Gate-to-Source Voltage

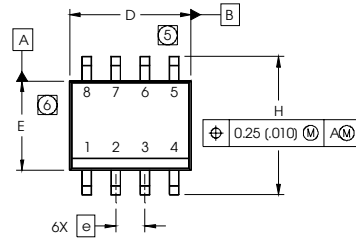


**Fig 22.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

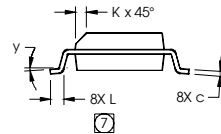
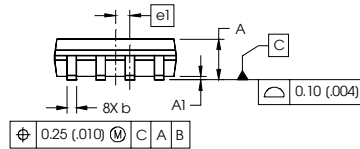


## SO-8 Package Outline

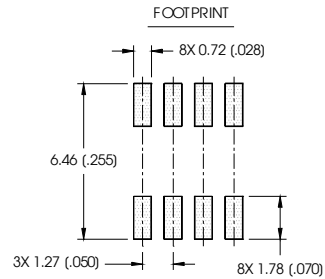
Dimensions are shown in millimeters (inches)



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°

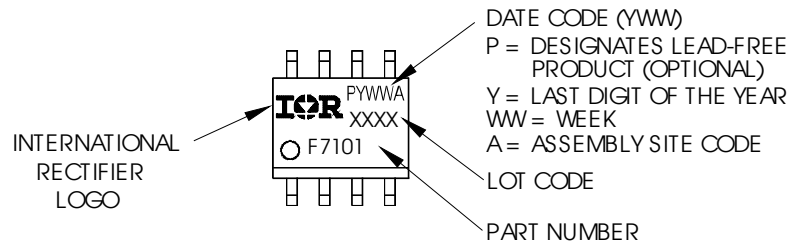


- NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
  2. CONTROLLING DIMENSION: MILLIMETER
  3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
  4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
  5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
  6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
  7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.



## SO-8 Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

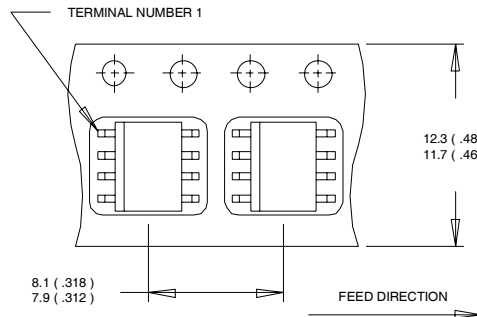


# IRF7343PbF

International  
**IOR** Rectifier

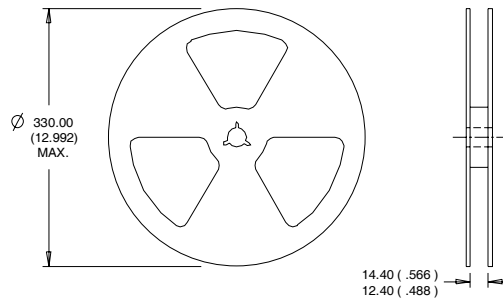
## SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.  
This product has been designed and qualified for the Consumer market.  
Qualifications Standards can be found on IR's Web site.

International  
**IOR** Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105  
TAC Fax: (310) 252-7903

Visit us at [www.irf.com](http://www.irf.com) for sales contact information.10/04

## **IMPORTANT NOTICE**

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office ([www.infineon.com](http://www.infineon.com)).

## **WARNINGS**

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.